## PRELIMINARY AMENDMENT Akito KURAMATA et al.

U.S. Patent Application S.N. 09/313,764 Attorney Docket No. 990527

a second cladding layer having a second, opposite conductivity type, said second cladding layer being formed on said active layer epitaxially;

a first electrode provided so as to inject first-type carriers having a first polarity into said second cladding layer; and

a second electrode provided on a bottom surface of said substrate so as to inject second-type carriers having a second polarity,

said buffer layer containing said first type carriers with a concentration level in the range from 3 x  $10^{18}$ cm<sup>-3</sup> to 1 x  $10^{20}$ cm<sup>-3</sup> and said compositional parameter x larger than 0 but smaller than 0.4 (0 < x < 0.4) so as to reduce an interface resistance between said substrate and said buffer layer.

## 21. (Amended) A semiconductor wafer, comprising:

an SiC substrate having an n-type conductivity; and

an AlGaN layer having an n-type conductivity formed on said SiC substrate with a composition represented as AlGa<sub>1-x</sub>N,

wherein said AlGaN layer has a carrier density in the range between 3 x  $10^{18}$  - 1 x  $10^{20} \text{cm}^{-3}$ , and

wherein said composition parameter x is larger than 0 but smaller than 0.4 (0 < x < 0.4) so as to reduce an interface resistance between said SiC substrate and said AlGaN layer.